

| | | | |
|-----------------------------------|-------------------------|---|-------------|
| Notice of References Cited | Application/Control No. | Applicant(s)/Patent Under Reexamination | |
| | 09/980,754 | LERCH ET AL. | |
| | Examiner | Art Unit | Page 1 of 1 |
| | Julio J. Maldonado | 2823 | |

U.S. PATENT DOCUMENTS

| * | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|--|-----------------|--------|----------------|
| A | US-5,967,794 | 10-1999 | Kodama | 438/300 |
| B | US- | | | |
| C | US- | | | |
| D | US- | | | |
| E | US- | | | |
| F | US- | | | |
| G | US- | | | |
| H | US- | | | |
| I | US- | | | |
| J | US- | | | |
| K | US- | | | |
| L | US- | | | |
| M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|--|-----------------|---------|------|----------------|
| N | | | | | |
| O | | | | | |
| P | | | | | |
| Q | | | | | |
| R | | | | | |
| S | | | | | |
| T | | | | | |

NON-PATENT DOCUMENTS

| | | | |
|---|--|--|--|
| * | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) | | |
| U | Wolf, Stanley and Tauber, Richard N.; Silicon processing for the VLSI Era Vol 1: Process Technology; 1986, Lattice press, pgs.192-194. | | |
| V | | | |
| W | | | |
| X | | | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.